AIDA Plans in Prague for WP9

Temperature compensated controller for bias voltage for SiPM detector

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Plan

- Characterisation of various SiPM style photodetectors, $G = f(T, \Delta V)$ with Bergen uni (N)

- Parametrisation of the influence $\Delta V = f(T)$

- Design of the controller

  - input: temp sensors

  - output: trimming voltage (DAC code)

- HV power supply - Market survey

- Prototyping of the controller

- Firmware issue, integration

- Final testing with Bergen uni

- Qualification